

	INFORMATION DISCLOSURE STATEMENT IN AN APPLICATION (Use several sheets if necessary)		Docket Number: 14934-49625	Serial Number: 10/531,231
			Applicant: Tajinder MANKU	Confirmation No.: 4880
			Filing Date: 15 October 2003	Group Art Unit: Unassigned

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/CC/	1.	6,606,359	08/12/03	Nag et al.			
/CC/	2.	4,250,458	02/10/81	Richmond et al.			
/CC/	3.	5,375,146	12/20/84	Chalmers			
/CC/	4.	5,793,817	08/11/98	Wilson			
/CC/	5.	5,548,840	08/20/96	Heck			
/CC/	6.	2002/050861 A1	05/02/02	Arnoldus et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
/CC/	7.	"Mini-Circuits Modern Mixer Terms Defined," 4 pages (1999).					
/CC/	8.	Bradshaw, P. "The ICL7650S: A New Era in Glitch-Free Chopper Stabilized Amplifiers," <i>Application Note</i> , Vol. AN053.2, pp. 1-14, July (2001).					
No date information.	9.	Au, T. et al., "Improved Flicker Noise Model for Submicron MOSFET Devices," <i>Department of Electrical Engineering and Computer Sciences, University of California at Berkeley</i>, pp. 1-6.					
No date information.	10.	Leenaerts, D. "Integrated Transceiver Design, Non-Linear Dynamic Issues," Phillips Research, 34 pages.					
/CC/	11.	Kim, B. et al., "Single-Ended Differential RF Circuit Topologies Utilizing Complementary MOS Devices," <i>Journal of Semi-Conductor Technology and Science</i> , Vol. 2, No. 1, pp. 7-18, March (2002).					
No date information.	12.	Consandinou, T., et al. "An Auto-Input Offset Removing Floating Gate Pseudo-Differential Transconductor," <i>EEE Dept., Imperial College of Science, Technology and Medicine, London</i>, 4 pages.					
/CC/	13.	Valero, A. et al., "Direct Conversion Receiver Implementation Issues," <i>Texas A&M University, Bluetooth Meeting</i> , 18 pages, March (2000).					
No date information.	14.	Luh, L. et al., "A Continuous-Time Common-Mode Feedback Circuit (CMFB) for High-Impedance Current-Mode Application," <i>Department of Electrical Engineering, University of Southern California</i>, 4 pages.					

EXAMINER /Charles Chow/	DATE CONSIDERED 06/01/2007
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.	

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No date information	15.	Tanaka, S. et al., "GSM/DCS1800 Dual Band Direct Conversion Transceiver IC With a DC Offset Calibration System" Bob Henshaw TTP Communications, 16 pages.
ICC/	16.	Strange, J. et al., "Direct Conversion: No Pain, No Gain," <i>Communication Systems Design</i> , 6 pages, April (2002).
No date information	17.	Fempel, M. et al., "A 3.6-V AlGaAs HBT Mixer for Wireless Applications," Berlin University of Technology, Microwave Engineering, 4 pages.
No date information	18.	Shahani, A. et al., "SP-22.3: A 12mW Wide-Dynamic-Range CMOS Front-End for a Portable GPS Receiver," Department of Electrical Engineering and Computer Sciences, University of California at Berkeley, 8 pages.
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ICC/	20.	Rudell, J. et al., "A 1.9GHz Wide-Band IF Double Conversion CMOS Integrated Receiver for Cordless Telephone Applications," <i>Department of Electrical Engineering and Computer Sciences, University of California at Berkeley</i> , 7 pages (2003).
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No date information	23.	Milner, L., "Baseband to RF Design of Ka-band Direct Conversion Transceivers for Digital Communications Systems," University of South Australia Cooperative Research Centre for Satellite Systems, 5 pages.
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